



PATENT
DOCKET NO.: P05779

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Kim et al.**
Title: **Method for Quantifying Safe Operating Area for Bipolar Junction Transistor**
Application No.: **10/688,550**
Filing Date: **October 17, 2003**

Commissioner for Patents
P. O. Box 1450
Alexandria, Virginia 22313-1450

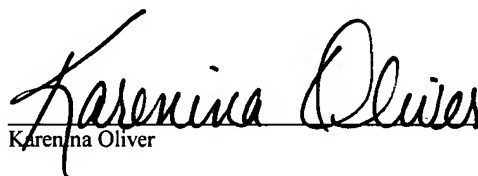
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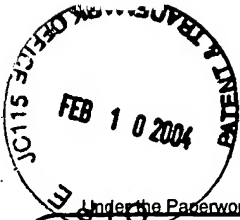
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Transmittal, Formal Drawings Transmittal, Formal Drawings (2 sheets) and return postcard.

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Karenina Oliver



PTO/SB/21 (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Application Number	10/688,550
Filing Date	10/17/03
First Named Inventor	Kim et al.
Art Unit	TBD
Examiner Name	TBD
Attorney Docket Number	P05779

Total Number of Pages in This Submission

ENCLOSURES (Check all that apply)

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under 37 CFR 1.52 or 1.53 | <input checked="" type="checkbox"/> Drawing(s)
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<input type="checkbox"/> Petition to Convert to a
Provisional Application
<input type="checkbox"/> Power of Attorney, Revocation
Change of Correspondence Address
<input type="checkbox"/> Terminal Disclaimer
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<input type="checkbox"/> CD, Number of CD(s) _____ | <input type="checkbox"/> After Allowance communication
to Group
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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Mark A. Dalla Valle Reg. No. 34,147
Signature	
Date	2/4/04

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Typed or printed name	Karenina Oliver
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Signature

Date 2/4/04

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of) Group Art Unit: TBD
KIM ET AL.)
Application No. 10/688,550) TRANSMITTAL OF FORMAL
Filed: October 17, 2003) DRAWINGS
For: METHOD FOR QUANTIFYING) Vedder, Price, Kaufman &
SAFE OPERATION AREA FOR) Kammholz, P.C.
BIPOLAR JUNCTION TRANSISTOR) 222 North LaSalle Street
Chicago, Illinois 60601
(312) 609-7500

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

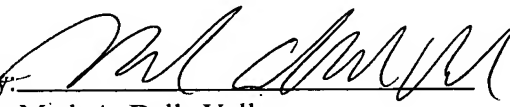
Please find enclosed are five (5) sheets of formal drawings for filing in the above-referenced case.

The Commissioner is hereby authorized to charge payment of any **additional** fees associated with this communication or credit any overpayment to Deposit Account No. 22-0259. **A duplicate copy of this sheet is enclosed for this purpose.**

Respectfully submitted,

VEDDER, PRICE, KAUFMAN & KAMMHOLZ, P.C.

Dated: February 4, 2004

By: 
Mark A. Dalla Valle
Reg. No. 34,147

Attorney for Applicant(s)

Atty. Docket No: P05779 (11461.00.5779)